

PCI Bus Preprocessors for HP Logic Analyzers

- Reliable 32- or 64-Bit PCI Bus Monitoring
- Complete State or Timing Analysis
- Automatic HP Logic Analyzer Configuration
- PCI Bus Transaction Inverse Assembly
- Simple and Convenient Logic Analyzer Connection
- Extender Card Capability For PCI Add-In Cards
- Lack of Active Buffers Provides Accurate Timing Analysis
- Diode Clamping Prevents Unwanted Reflections
- PC Mapper Software Decodes Common PC Accesses

Designers incorporating the new Intel i960® RP processor into a Pentium® processor or P6-based server will find the FuturePlus Systems PCI Preprocessor's features invaluable for debugging and compliance-testing both the primary and secondary PCI buses. The FS16P32E and FS16P64 include a mechanical and electrical interface between the PCI bus and an HP logic analyzer, as well as software that automatically configures the HP logic analyzer correctly, and decodes all the signals on the bus into easy-to-read mnemonics. The inverse assembler also performs register decode, has support for symbols and the HP Symbol Download Utility, and includes post-processing filters (HP 16500B, 16505A, and 1660A only). In addition, an enhanced transaction inverse assembler called PC Mapper is included that decodes common PC accesses such as keyboard, mouse, BIOS, etc.

FuturePlus Systems also offers PMC, ISA and Micro Channel preprocessors. Using these with your PCI preprocessor will allow you to see both buses simultaneously. Further, coordinated measurements may be made with HP preprocessors monitoring the host processor such as the Pentium or P6 processor.



HOST SYSTEMS SUPPORTED:
HP 1660-series, HP 16500-series

PROCESSORS SUPPORTED:
i960 RP Processor

AVAILABILITY:
Now

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